

U.S. Department of Commerce, Patent and Trademark Office	Application No.:
	Filing date: March 12, 2002
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Boucher, et al.
	Group Art Unit:
TCP/IP Offload Network Interface Device	Examiner name: Unknown
Express Mail: EL928548975US	Attorney Docket No. ALA-009



U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	4,366,538	12/28/82	Johnson et al.	364	200	
EJK	B	4,991,133	05/13/86	Davis et al.	364	900	
EJK	C	5,056,058	10/08/91	Hirata et al.	709	230	
EJK	D	5,097,442	03/17/92	Ward et al.	365	78	
EJK	E	5,163,131	11/10/92	Row et al.	395	200	
EJK	F	5,212,778	05/18/93	Dally et al.	395	400	
EJK	G	5,280,477	01/18/94	Trapp	370	85.1	
EJK	H	5,289,580	02/22/94	Latif et al.	395	275	
EJK	I	5,303,344	04/12/94	Yokoyama et al.	395	275	
EJK	J	5,412,782	05/02/95	Hausman et al.	395	250	
EJK	K	5,448,566	09/05/95	Richter et al.	370	94.1	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
EJK	L	WO 00/13091	03/09/00	PCT/US98/24943			
EJK	M	WO 99/65219	12/16/99	PCT/US99/13184			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	N	Internet page entitled "Hardware Assisted Protocol Processing", (which Eugene Feinberg is working on), 1 page, printed 11/25/98.
EJK	O	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
EJK	P	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.
EJK	Q	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, 7 pages, printed 5/27/97.

Examiner <i>Lin</i>	Date Considered 1/26/2006
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U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	5,485,579	01/16/96	Hitz et al.	395	200.12	
EJK	B	5,506,966	04/09/96	Ban	395	250	
EJK	C	5,511,169	04/23/96	Suda	395	280	
EJK	D	5,548,730	08/20/96	Young et al.	395	280	
EJK	E	5,588,121	12/24/96	Reddin et al.	395	200.15	
EJK	F	5,566,170	10/15/96	Bakke et al	370	60	
EJK	G	5,590,328	12/31/96	Seno et al.	395	675	
EJK	H	5,592,622	01/07/97	Isfeld et al.	395	200.02	
EJK	I	5,629,933	05/13/97	Delp et al.	370	411	
EJK	J	5,634,099	05/27/97	Andrews et al	395	200.07	
EJK	K	5,634,127	05/27/97	Cloud et al.	395	680	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
EJK	L	WO 01/05107 A1	01/18/01	PCT/US00/19006			
EJK	M	WO 01/05116 A2	01/18/01	PCT/US00/19243			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	N	Internet pages entitled: "A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University," 13 pages, printed 12/21/98.
EJK	O	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
EJK	P	Internet pages entitled: "Northridge/Southbridge vs. Intel Hub Architecture," 4 pages, printed 2/19/01.
EJK	Q	Gigabit Ethernet Technical Brief, "Achieving End-to-End Performance," Alteon Networks, Inc., First Edition, September 1996.

Examiner <i>W. Kim</i>	Date Considered 1/26/2006
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	5,642,482	06/24/97	Pardillos	395	200.2	
EJK	B	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
EJK	C	5,671,355	09/23/97	Collins	395	200.2	
EJK	D	5,678,060	10/14/97	Yokoyama et al.	709	212	
EJK	E	5,692,130	11/25/97	Shobu et al.	395	200.12	
EJK	F	5,699,317	12/16/97	Sartore et al.	395	230.06	
EJK	G	5,701,434	12/23/97	Nakagawa	395	484	
EJK	H	5,701,516	12/23/97	Cheng et al.	395	842	
EJK	I	5,749,095	05/05/98	Hagersten	711	141	
EJK	J	5,751,715	05/12/98	Chan et al.	370	455	
EJK	K	5,752,078	05/12/98	Delp et al.	395	827	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
EJK	L	WO 01/05123 A1	01/18/01	PCT/US00/18976			
EJK	M	WO 01/40960A1	06/07/01	PCT/US00/32660			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	N	Internet pages directed to "Technical Brief on Alteon Ethernet Gigabit NIC Technology," www.alteon.com, 14 pages, printed 3/15/97.
EJK	O	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
EJK	P	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", http://www.ireadyco.com/archives/keyexec.html, 2 pages, dated 8/20/98, printed 11/28/98.
EJK	Q	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998, 3 pages, printed 11/28/98.

Examiner <i>Li</i>	Date Considered <i>1/26/2006</i>
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U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
ESK	A	5,758,084	05/26/98	Silverstein et al.	395	200.58	
ESK	B	5,758,089	05/26/98	Gentry et al.	395	200.64	
ESK	C	5,758,186	05/26/98	Hamilton et al.	395	831	
ESK	D	5,758,194	05/26/98	Kuzma	395	886	
ESK	E	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
ESK	F	5,790,804	08/04/98	Osborne	395	200.75	
ESK	G	5,794,061	08/11/98	Hansen et al.	395	800.01	
ESK	H	5,802,580	09/01/98	McAlpice	711	149	
ESK	I	5,809,328	09/15/98	Nogales et al.	395	825	
ESK	J	5,812,775	09/22/98	Van Seeters et al.	395	200.43	
ESK	K	5,815,646	09/29/98	Purcell et al.	395	163	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
ESK	L	WO 01/04770 A2	01/18/01	PCT/US00/18939			
ESK	M	WO/98/19412	05/07/98	PCT/US97/17257			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
ESK	N	Internet pages from iReady Products, "The Internet Tuner" and other sections, website http://www.ireadyco.com/products.html , 2 pages, downloaded 11/25/98.
ESK	O	iReady News Archives, "Toshiba, iReady Shipping Internet Chip," by Bob Guth, 1 page, printed 12/28/98.
ESK	P	Interprophet article entitled "Technology", http://www.interprophet.com/technology.html , 17 pages, printed 3/1/00.
ESK	Q	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.

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EJK	A	5,878,225	03/02/99	Bilansky et al.	395	200.57	
EJK	B	5,913,028	06/15/99	Wang et al.	395	200.33	
EJK	C	5,930,830	07/27/99	Mendelson et al.	711	171	
EJK	D	5,931,918	08/03/99	Row et al	709	300	
EJK	E	5,935,205	08/10/99	Murayama et al.	709	216	
EJK	F	5,937,169	08/10/99	Connery et al.	395	200.8	
EJK	G	5,941,969	08/24/99	Ram et al.	710	128	
EJK	H	5,941,972	08/24/99	Hoese et al.	710	129	
EJK	I	5,950,203	09/07/99	Stakuis et al.	707	10	
EJK	J	5,991,299	11/23/99	Radogna et al.	370	392	
	K						

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
EJK	L	WO/98/50852	11/12/98	PCT/US98/08719			
EJK	M	WO/99/04343	01/28/99	PCT/US98/14729			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	N	iReady article entitled "About Us Introduction", Internet pages from http://www.iReadyco.com/about.html , 3 pages, printed 11/25/98.
EJK	O	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", by Brogan and Johnson, November 20, 1997, 2 pages.
EJK	P	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," by Endo et al., October 26, 1998. 2 pages, printed 11/2/98.
EJK	Q	NEWSwatch article entitled "iReady Internet Tuner to Web Enable Devices", November 5, 1996, printed 11/2/98.

Examiner <i>EJK</i>	Date Considered <i>1/26/2004</i>
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U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
ESK	A	6,005,849	12/21/99	Roach et al.	370	276	
ESK	B	6,009,478	12/28/99	Panner et al.	710	5	
ESK	C	6,016,513	01/18/00	Lowe	709	250	
ESK	D	6,026,452	02/15/00	Pitts	710	56	
ESK	E	6,034,963	03/07/00	Minami et al.	370	401	
ESK	F	6,044,438	03/28/00	Olnowich	711	130	
ESK	G	6,047,356	04/04/00	Anderson et al.	711	129	
ESK	H	6,057,863	05/02/00	Olarig	345	520	
ESK	I	6,061,368	05/09/00	Hitzelberger	370	537	
ESK	J	6,065,096	05/16/00	Day et al.	711	114	
ESK	K	6,141,705	10/31/00	Anand et al.	710	15	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
	L						
	M						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
ESK	N	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
ESK	O	"Comparison of Novell Netware and TCP/IP Protocol Architectures", by J.S. Carbone, 19 pages, 7/16/95, printed 4/10/98.
ESK	P	Adaptec article entitled "AEA-7110C-a DuraSAN product", 11 pages, printed 10/1/01.
ESK	Q	iSCSI HBA article entitled "iSCSI and 2Gigabit Fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.

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EJK	A	6,246,683	06/12/01	Connery et al.	370	392	
EJK	B	6,247,060	06/12/01	Boucher et al.	709	238	
EJK	C	60/053,240		Jolitz et al.			07/18/97
EJK	D	08/964,304		Napolitano, et al.			11/04/97
EJK	E	6,246,683	06/12/01	Connery et al.	370	392	
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Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	N	iSCSI HBA article directed to JNI iSCSI HBAs, entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channel HBA", 6 pages, printed 10/01/01.
EJK	O	ISCSI.com article that mentions Emulex HBAs, entitled "iSCSI Storage", 2 pages, printed 10/01/01.
EJK	P	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, pages 729-743, December 1998.
EJK	Q	IReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", by Sugiyama et al., 3 pages, 10/14/98, printed 11/2/98.

Examiner <i>Lin</i>	Date Considered <i>1/26/2006</i>
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Foreign Patent Documents								
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
ESK	N	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
ESK	O	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
ESK	P	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.
	Q	

Examiner <i>li/lin</i>	Date Considered <i>1/26/2006</i>
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Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
	L						
	M						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
ESK	N	Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.
ESK	O	CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.
ESK	P	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
ESK	Q	CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.

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Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
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	M						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
ESK	N	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
ESK	O	Internet pages entitled "Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, printed 6/5/97.
ESK	P	Jato Technologies article entitled "Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98.
ESK	Q	EETimes article entitled "Enterprise System Uses Flexible Spec, dated August 10, 1998, printed 11/25/98.

Examiner <i>Li Li -</i>	Date Considered <i>1/26/2006</i>
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Foreign Patent Documents								
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	H							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EJK	I	Internet pages entitled "Smart Ethernet Network Interface Card", which Berend Ozceri is developing, printed 11/25/98.
EJK	J	Internet pages of Xaqti Corporation entitled "GigaPower Protocol Processor Product Review," printed 11/25/98.
EJK	K	Internet pages entitled "DART: Fast Application Level Networking via Data-Copy Avoidance," by Robert J. Walsh, printed 6/3/99.
EJK	L	Internet pages of InterProphet entitled "Frequently Asked Questions", by Lynne Jolitz, 4 pages, printed 6/14/99.
EJK	M	Internet pages entitled "iReady Products," printed 11/25/98.
EJK	N	Andrew S. Tanenbaum, "Computer Networks," Third Edition, 1996, ISBN 0-13-349945-6, (entire book).

Examiner <i>W K</i>	Date Considered <i>1/26/2006</i>
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ESK	A	5,996,024	11/30/99	Blumenau	709	301	
ESK	B	6,226,680	05/01/01	Boucher et al.	709	230	
ESK	C	US2001/0004354A1	06/21/01	Jolitz	370	328	
ESK	D	6,173,333	01/09/01	Jolitz, et al.	709	240	
ESK	E	US2001/0025315A1	09/27/01	Jolitz	709	231	
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	G						
	H						
	I						
	J						

Foreign Patent Documents

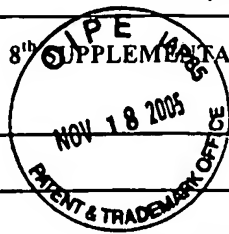
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	K							
	L							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/098,694
	Filing date: March 12, 2002
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: 2154
TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-009C

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	1	5,212,778	5/18/93	Dally et al.	395	400	
EJK	2	5,642,482	6/24/97	Pardillos	395	200.2	
EJK	3	5,878,225	3/2/99	Bilansky et al.	395	200.57	
EJK	4	5,970,804	10/26/99	Osborne	395	200.75	
EJK	5	6,067,569	5/23/00	Khaki et al.	709	224	
EJK	6	6,101,555	8/8/00	Goshey et al.	709	321	
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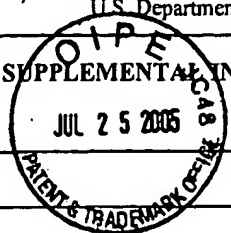
OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
EJK	A	Thia, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", <u>Protocols for High Speed Networks</u> , pages 53-68, 1993.
EJK	B	Thia, Y.H. Publication entitled " A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", <u>Protocols for High Speed Networks</u> , pages 224-239, 1995.
	C	
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Examiner 

Date Considered 1/25/2006


*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/098,694
 7 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: March 12, 2002
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: 2154
TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-009C

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	2003/0165160	9/4/03	Minami et al.	370	466	
EJK	B	2003/0066011	4/11/02	Oren	714	758	
EJK	C	2001/0014954	8/16/01	Purcell et al.	714	4	
EJK	D	5,598,410	1/28/1997	Stone	370	469	
EJK	E	6,389,479	5/14/02	Boucher	709	243	
EJK	F	6,421,742	7/16/02	Tillier	710	1	
EJK	G	6,480,489	11/12/02	Muller et al.	370	389	
EJK	H	6,526,446	2/25/03	Yang et al.	709	230	
EJK	I	6,145,017	11/7/00	Ghaffari	710	5	
EJK	J	6,157,955	12/5/00	Narad et al.	709	228	
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OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
EJK	1	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
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Examiner 	Date Considered 1/25/2006	

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/098,694
<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p>6th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>CANCELLED</p> <p>JUN 15 2005</p> <p>PATENT & TRADEMARK OFFICE</p> </div> <div style="text-align: center;"> <p>JUN 29 2005</p> <p>PATENT & TRADEMARK OFFICE</p> </div> </div>	Filing date: March 12, 2002
	Inventors: Laurence B. Boucher, et al.
MIP OFFLOAD NETWORK INTERFACE DEVICE	Group Art Unit: 2154
	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-009C

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	2001/0053148	12/20/01	Billic et al.	370	389	
EJK	B	2001/0048681	12/6/01	Billic et al.	370	389	
EJK	C	2001/0014892	8/16/01	Gaither et al.	707	200	
EJK	D	2001/0013059	8/9/01	Dawson et al.	709	217	
EJK	E	2001/0004354	01/10/01	Jolitz	370	328	
EJK	F	4,589,063	5/13/86	Shah et al.	710	8	
EJK	G	5,778,013	7/7/98	Jedwab	714	807	
EJK	H	5,970,804	8/4/98	Osborne	395	200.75	
EJK	I	6,038,562	3/14/00	Anjur et al.	707	10	
EJK	J	6,070,200	5/30/00	Gates et al.	710	20	
EJK	K	6,101,555	8/8/00	Goshey et al.	709	321	
EJK	L	6,145,017	11/7/00	Ghaffari	710	5	
EJK	M	6,157,955	12/5/00	Narad et al.	709	228	
EJK	N	6,172,980	1/9/01	Flanders et al.	370	401	
EJK	O	6,202,105	3/13/01	Gates et al.	710	20	
EJK	P	6,279,051	8/21/01	Gates et al.	710	20	
EJK	Q	6,298,403	10/2/01	Suri et al.	710	100	
EJK	R	6,334,153	12/25/01	Boucher et al.	709	230	

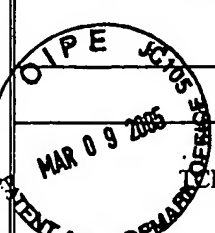
Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
EJK	S	WO 01/59966	8/16/01	PCT/US00/06475				
EJK	T	WO 01/86430	11/15/01	PCT/US01/15180				

Examiner *L. L.*Date Considered *1/26/2006*

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U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/098,694
6 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: March 12, 2002
		Inventors: Laurence B. Boucher, et al.
		Group Art Unit: 2154
TCP/IP OFFLOAD NETWORK INTERFACE DEVICE		Examiner name: Larry D. Donaghue
		Attorney Docket No. ALA-009C
OTHER ART—NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
ESK	1	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
ESK	2	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
ESK	3	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
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Examiner <i>ESK</i>		Date Considered 1/26/2006
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>		

U.S. Department of Commerce, Patent and Trademark Office		Application No.: 10/098,694
5 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: March 12, 2002
		Inventors: Laurence B. Boucher, et al.
 TCP/IP OFFLOAD NETWORK INTERFACE DEVICE		Group Art Unit: Unknown
		Examiner name: Zarni Maung
		Attorney Docket No. ALA-009C

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	5,524,250	6/4/96	Chesson et al.	395	775	
EJK	B	5,619,650	4/8/97	Bach et al.	395	200.01	
EJK	C	5,727,142	3/10/98	Chen	395	181	
EJK	D	5,802,258	9/1/98	Chen	395	182.08	
EJK	E	5,898,713	4/27/99	Melzer et al.	371	53	
EJK	F	6,021,507	2/1/00	Chen	714	2	
EJK	G	6,047,323	4/4/00	Krause	709	227	
	H						

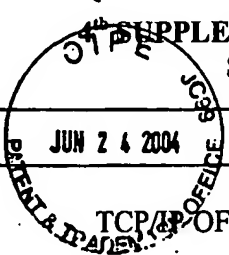
OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
EJK	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
EJK	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
EJK	3	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
EJK	4	Macleane et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols"; 7 pages, Dec. 2-5, 1991.
EJK	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
EJK	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
EJK	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
EJK	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
EJK	9	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.

Examiner *Li Lin*

Date Considered 1/25/2006


*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office <div style="display: flex; align-items: center;">  <div> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT </div> </div>	Application No.: 10/098,694 Filing date: March 12, 2002 Inventors: Laurence B. Boucher, et al. Group Art Unit: Unknown Examiner name: Zarni Maung Attorney Docket No. ALA-009C
TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	

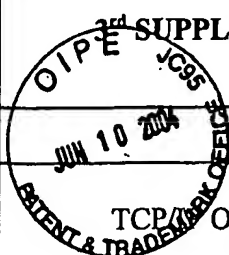
U.S. Patent Documents							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
ESK	A	5,517,668	5/14/1996	Szwerinski et al.	395	800	
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Foreign Patent Documents							
							Translation
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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Examiner 	Date Considered 11/25/2006
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/098,694
<div style="text-align: center;">  <p>3rd SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>TCP/IP OFFLOAD NETWORK INTERFACE DEVICE</p> </div>	Filing date: March 12, 2002
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: Unknown
	Examiner name: Zarni Maung
Attorney Docket No. ALA-009C	


U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
ESK	A	5,598,410	1/28/1997	Stone	370	469	
	B						
	C						
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Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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Examiner 	Date Considered 1/26/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office <div style="display: flex; align-items: center; justify-content: center;">  <div style="margin-left: 20px;"> SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT </div> </div>	Application No.: 10/098,694 Filing date: March 12, 2002 Inventors: Laurence B. Boucher, et al. Group Art Unit: Unknown Examiner name: Zarni Maung Attorney Docket No. ALA-009C
TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	

U.S. Patent Documents


*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
EJK	A	5,058,110	10/15/91	Beach et al.	370	85.6	
EJK	B	6,021,446	02/01/00	Gentry, Jr.	709	303	
EJK	C	6,356,951	03/12/02	Gentry, Jr.	709	250	
EJK	D	6,389,468	05/14/02	Muller et al.	709	226	
EJK	E	6,427,169	07/30/02	Elzur	709	224	
EJK	F	6,434,651	08/13/02	Gentry, Jr.	710	260	
EJK	G	6,449,656	09/10/02	Elzur et al.	709	236	
EJK	H	6,453,360	09/17/02	Muller et al.	709	250	
EJK	I	6,345,301	02/05/02	Burns et al.	709	230	
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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EJK	N	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, Rice University, October 1996, 15 pages.
EJK	O	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
	P	
	Q	

Examiner 	Date Considered 1/25/2006
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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 10/098,694

Filing date: March 12, 2002

Inventors: Laurence B. Boucher et al.

Group Art Unit: Unknown

Examiner name: Unknown

Attorney Docket No. ALA-009C

SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

TITLE/IP OFFLOAD NETWORK INTERFACE DEVICE

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents

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		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EJK	N	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 pages).
EJK	O	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1988 (10 pages).
	P	
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Examiner

L. Kim

Date Considered

1/25/2006

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